

Notice of References Cited	Application/Control No. 09/885,761		Applicant(s)/Patent Under Reexamination KUWAZAWA, KAZUNOBU	
	Examiner Stanetta D. Isaac		Art Unit 2812	Page 1 of 1

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, Lattice Press, Vol. I, pages 522-535
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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